AMENDMENTS TO THE CLAIMS:

1. (Previously Presented) An information processing apparatus, comprising:

first and second information processing means for performing a same process in synchronism with each other; and

adjustment means for adjusting orders of output data from said first and second information processing means so as to correspond to each other to discriminate whether or not the output data coincide with each other.

2. (Original) An information processing apparatus as claimed in claim 1, wherein said adjustment means includes first storage means for storing the output data of said first information processing means and second storage means for storing the output data of said second information processing means.

3. (Previously Presented) An information processing apparatus as claimed in claim 2, wherein said adjustment means compares, when an amount of output data stored in any one of said first and second storage means reaches a predetermined amount, the output data of said first information processing means stored in said first storage means and the output data of said second information processing means stored in said second storage means with each other, with the output data adjusted in order so as to correspond to each other to discriminate whether or not the output data coincide with each other.

4. (Previously Presented) An information processing apparatus as claimed in claim 2,

wherein said adjustment means further includes designation means for designating a frequency with which the discrimination is to be performed to a frequency lower than a frequency with which the output data of said first and second information processing means are received.

5. (Previously Presented) An information processing apparatus, comprising:

first and second information processing means for performing a same process in synchronism with each other;

and

adjustment means including re-construction means for re-constructing a plurality of output data of said second information processing means based on a plurality of output data of said first information processing means; and

comparison means for comparing with each other the output data of said first information processing means and the output data of said second information processing means re-constructed by said re-construction means.

6. (Previously Presented) An information processing apparatus as claimed in claim 5, wherein said adjustment means includes first storage means for storing the output data of said first information processing means and second storage means for storing the output data of said second information processing means, and said re-construction means changes an order of the output data of said second information processing means stored in said second storage means based on the order of the output data of said first information processing means stored in said first storage means.

7. (Original) An information processing apparatus as claimed in claim 5, wherein said adjustment means includes first storage means for storing the output data of said first information processing means and second storage means for storing the output data of said second information processing means, and said re-construction means divides and re-couples the output data of said second information processing means stored in said second storage means based on the output data of said first information processing means stored in said first storage means.

8. (Currently Amended) An information processing apparatus, comprising:

first and second information processing means for performing a same process in synchronism with each other; and

adjustment means for selecting one ~~of data~~ of a plurality of data of a second output of said second information processing means which data is determined to correspond to one of data of a plurality of data of a first output of said first information processing means to detect whether or not the data of the first and second outputs coincide with each other.

9. (Original) An information processing apparatus as claimed in claim 8, wherein said adjustment means includes first storage means for storing the data of the first output of said first information processing means and second storage means for storing the data of the second output of said second information processing means, and said adjustment means searches said second storage means for one of the data of the second output corresponding to one of the data of the first output of said first information processing means stored in said

first storage means.

10.-19 (Canceled)

20. (New) The information processing apparatus according to claim 1, wherein said adjustment means comprises error control means controlling an output of said adjustment means such that one of said output data of said first and second information processing means is outputted from said adjustment means.

21. (New) The information processing apparatus according to claim 20, wherein:

said error control means receives notifications of whether or not orders of said output data coincide with each other,

said error control means receives notifications of whether or not data values of said output data coincide with each other, and

said error control means receives notifications of whether or not an error of hardware failure exists.

22. (New) The information processing apparatus according to claim 21, wherein said error of hardware failure may be a parity error, a protocol error, or a timeout of a packet, of said first or second information processing means.

23. (New) The information processing apparatus according to claim 21, wherein in a case

that said error control means receives a notification of incoincidence of said orders of said output data, or said error control means receives a notification of incoincidence of said data values of said output data, and said error control means does not receive a notification of an error of hardware failure, then said error control means executes a synchronism restoration process for said first and second information processing means.

24. (New) The information processing apparatus according to claim 21, wherein in a case that said error control means receives a notification of an error of hardware failure of one of said first or second information processing means, then said error control means performs a disconnection process of said one of said first and second information processing means.